



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/909,111  
Filing Date ..... July 18, 2001  
Inventor ..... Mark Fischer  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 1765  
Examiner ..... Duy Deo  
Attorney's Docket No. .... MI22-1777  
Title: Semiconductor Processing Methods of Forming a Conductive Projection  
and Methods of Increasing Alignment Tolerances

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References –See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449, a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 8/21/03

By: 

Mark S. Matkin  
Reg. No. 32,268

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